



## PATENT

O.P.E. 2 PATENT DATE 11/1/85  
 SCANNED 6/25 Q.A. HT

APPLICATION NO.	CONTR/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/827015	D	716	17	2885 2816	Lin h Nguyen

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TITLE

Depopulated programmable logic array

PTO-2040  
12/99

<b>ISSUING CLASSIFICATION</b>									
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>									

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b> Total Claims      Print Claim for O.G.	
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner)      (Date)		<b>ISSUE FEE</b>	
			Amount Due      Date Paid	
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      (Date)		<b>ISSUE BATCH NUMBER</b>	
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